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CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, with sufficient postage, in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the below date:

Date: December 13, 2005 Name: Anthony P. Curtis, Ph.D., 46,193 Signature:

BRINKS
HOFER
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& LIONE

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Appln. of: Thomas Benetik et al.

Appln. No.: 10/512,017

Examiner: N/A

Filed: April 29, 2005

Art Unit: 2812

For: Semiconductor Component with Integrated Capacitance Structure and Method for Fabrication Thereof

Attorney Docket No: 10808/169

Attn: Application Processing Division Customer Correction Branch
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL

Sir:

Attached is/are:

Transmittal Cover Letter (1p. Filed in Dup.); Request for Correction of Filing Receipt (2pp. Filed in Dup.); Copy of Filing Receipt with Corrections (2pp.); Copy of Previously Filed Declaration (3pp.)
 Return Receipt Postcard

Fee calculation:

No additional fee is required.
 Small Entity.
 An extension fee in an amount of \$_____ for a _____-month extension of time under 37 C.F.R. § 1.136(a).
 A petition or processing fee in an amount of \$_____ under 37 C.F.R. § 1.17(____).
 An additional filing fee has been calculated as shown below:

	Claims Remaining After Amendment		Highest No. Previously Paid For	Present Extra	Small Entity		Not a Small Entity		
					Rate	Add'l Fee	or	Rate	Add'l Fee
Total		Minus			x \$25=			x \$50=	
Indep.		Minus			x 100=			x \$200=	
First Presentation of Multiple Dep. Claim					+\$180=			+\$360=	
					Total	\$		Total	\$

Fee payment:

A check in the amount of \$_____ is enclosed.
 Please charge Deposit Account No. 23-1925 in the amount of \$_____. A copy of this Transmittal is enclosed for this purpose.
 Payment by credit card in the amount of \$_____ (Form PTO-2038 is attached).
 The Director is hereby authorized to charge payment of any additional filing fees required under 37 CFR § 1.16 and any patent application processing fees under 37 CFR § 1.17 associated with this paper (including any extension fee required to ensure that this paper is timely filed), or to credit any overpayment, to Deposit Account No. 23-1925.

Respectfully submitted,

Anthony P. Curtis, Ph.D. (Reg. No. 46,193)

12/3/05



Serial No. 10/512,017
Attorney Docket No. 10808/169

I hereby certify that this correspondence is being deposited with the United States Postal Service, with sufficient postage, as first class mail in an envelope addressed to:
Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

on December 13, 2005

Date of Deposit

Anthony P. Curtis, Ph.D., Reg. No. 46,193

Name of applicant, assignee or
Registered Representative

Signature

12/13/05

Date of Signature

Our Case No. 10808/169

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Thomas Benetik et al.

Serial No. 10/515,017

Filing Date: April 29, 2005

For Semiconductor Component with
Integrated Capacitance Structure
and Method for Fabrication Thereof

Examiner N/A

Group Art Unit No. 2812

REQUEST FOR CORRECTION OF FILING RECEIPT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attention: Application Processing Division
Customer Correction Branch

Sir:

Applicant requests the issuance of a corrected filing receipt (copy enclosed) for the above-referenced patent application, and in support of this request respectfully states:

The title should be "**SEMICONDUCTOR COMPONENT WITH
INTEGRATED CAPACITANCE STRUCTURE AND METHOD FOR**

Serial No. 10/512,017
Attorney Docket No. 10808/169

**FABRICATION THEREOF" not "SEMICONDUCTOR COMPONENT
HAVING AN INTEGRATED CAPACITANCE STRUCTURE AND
METHOD FOR PRODUCING THE SAME" (see attached Declaration)**

The Commissioner is hereby authorized to charge any fees required to
Deposit Account No. 23-1925. A duplicate copy of this sheet is enclosed.

Respectfully submitted,



Anthony P. Curtis, Ph.D.
Registration No. 46,193
Attorney for Applicant

BRINKS HOFER GILSON & LIONE
P.O. BOX 10395
CHICAGO, ILLINOIS 60610
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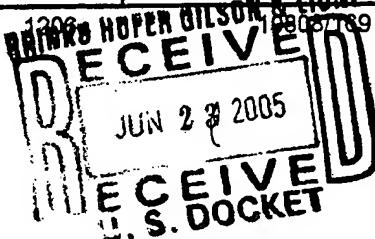
UNITED STATES PATENT AND TRADEMARK OFFICE

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United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/512,017	04/29/2005	2812		1206 HOFER GILSON & LIONE 148067789	6	27	2

CONFIRMATION NO. 8467

48581
BRINKS HOFER GILSON & LIONE
INFINEON
PO BOX 10395
CHICAGO, IL 60610



Date Mailed: 06/17/2005

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please mail to the Commissioner for Patents P.O. Box 1450 Alexandria Va 22313-1450. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate). . .

Applicant(s)

Thomas Benetik, Munchen, GERMANY;
Erwin Ruderer, Markt Schwaben, GERMANY;

Power of Attorney: The patent practitioners associated with Customer Number **48581**.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/DE03/00964 03/24/2003

Foreign Applications

GERMANY 102 17 567.5 04/19/2002

Projected Publication Date: 09/22/2005

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor Component with Integrated Capacitance Structure
And Method for Fabrication Thereof
Semiconductor component having an integrated capacitance structure and method for producing
the same

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Preliminary Class

438

**LICENSE FOR FOREIGN FILING UNDER
Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

GRANTED

The applicant has been granted a license under 35 U.S.C. 184. If the phrase "IF REQUIRED, FOREIGN FILING LICENSE GRANTED" followed by a date appears on this form. Such licenses are issued in all applications where the conditions for issuance of a license have been met, regardless of whether or not a license may be required as set forth in 37 CFR 5.15. The scope and limitations of this license are set forth in 37 CFR 5.15(a) unless an earlier license has been issued under 37 CFR 5.15(b). The license is subject to revocation upon written notification. The date indicated is the effective date of the license, unless an earlier license of similar scope has been granted under 37 CFR 5.13 or 5.14.

This license is to be retained by the licensee and may be used at any time on or after the effective date thereof unless it is revoked. This license is automatically transferred to any related applications(s) filed under 37 CFR 1.53(d). This license is not retroactive.

The grant of a license does not in any way lessen the responsibility of a licensee for the security of the subject matter as imposed by any Government contract or the provisions of existing laws relating to espionage and the national security or the export of technical data. Licensees should apprise themselves of current regulations especially with respect to certain countries, of other agencies, particularly the Office of Defense Trade Controls, Department of State (with respect to Arms, Munitions and Implements of War (22 CFR 121-128)); the Office of Export Administration, Department of Commerce (15 CFR 370.10 (j)); the Office of Foreign Assets Control, Department of Treasury (31 CFR Parts 500+) and the Department of Energy.

NOT GRANTED

No license under 35 U.S.C. 184 has been granted at this time, if the phrase "IF REQUIRED, FOREIGN FILING LICENSE GRANTED" DOES NOT appear on this form. Applicant may still petition for a license under 37 CFR 5.12, if a license is desired before the expiration of 6 months from the filing date of the application. If 6 months has lapsed from the filing date of this application and the licensee has not received any indication of a secrecy order under 35 U.S.C. 181, the licensee may foreign file the application pursuant to 37 CFR 5.15(b).



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Infineon Ref No. I0349US
OC Ref No. 10808/169

DECLARATION FOR PATENT APPLICATION, POWER OF ATTORNEY & DESIGNATION OF CORRESPONDENCE ADDRESS

As below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

SEMICONDUCTOR COMPONENT WITH INTEGRATED CAPACITANCE STRUCTURE AND METHOD FOR FABRICATION THEREOF

the specification of which (check one)

is attached hereto.

was filed on October 19, 2004 as Application Serial No. 10/512,017
and was amended on October 19, 2004.

I hereby state that I have reviewed and understand the contents of the above-identified specification including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information known to me to be material to the patentability of this application in accordance with 37 CFR §1.56(a).

PRIOR FOREIGN/PCT APPLICATION(S) FILED WITHIN 12 MONTHS (6 MONTHS FOR DESIGN) PRIOR TO THIS APPLICATION AND ANY PRIORITY CLAIMS UNDER 35 U.S.C. § 119(a)-(d)

I hereby claim foreign priority benefits under 35 USC § 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)			Priority Claimed	
Number	Country	Day/Month/Year Filed	Yes	No
102 17 567.5	Germany	April 19, 2002	X	

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CLAIM FOR BENEFIT OF PRIOR U.S. PROVISIONAL APPLICATION(S)
(35 U.S.C. § 119(e))

I hereby claim the benefit under Title 35, United States Code, § 119 (e) of any United States provisional application(s) listed below:

Provisional Application Number	Filing Date

CLAIM FOR BENEFIT OF EARLIER US/PCT APPLICATION(S)
UNDER 35 U.S.C. § 120

I hereby claim the benefits under 35 USC § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of 35 USC § 112, I acknowledge the duty to disclose material information as defined in 37 CFR § 1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

Appl. No.	Filing Date	Patented, Pending, Abandoned
PCT/DE 03/00964	March 24, 2003	Pending

Power of Attorney: As a named inventor, I hereby appoint the practitioners under Customer Number

Customer No. 48581- Infineon Technologies

jointly, and each of them severally, my attorneys at law/patent agent(s), with full power of substitution, delegation and revocation, to prosecute this application and any continuations, divisions, reissues, and reexaminations thereof, to receive the patent(s), to transact all business in the United States Patent and Trademark Office connected therewith and to act on my behalf before the competent International Authorities in connection with any and all international applications filed.

Please recognize or change the correspondence address for this application to the address associated with the above-mentioned Customer Number. Please direct all telephonic and facsimile communications to:

Stephen Charles Smith
 Tel.: (312) 321-4250; Fax: (312) 321-4299

I hereby declare that all statements made herein on my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both under 18 USC §1001 and that such

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willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Full Name of First Inventor: Thomas Benetik	
Inventor's Signature: <i>Thomas Benetik</i>	Date: 11.7.05
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Citizenship: Austria	
Mailing Address: Thalkirchner Str. 7, D-80337 München, Germany	

Full Name of Second Inventor: Erwin Ruderer	
Inventor's Signature: <i>Erwin Ruderer</i>	Date: 11.7.05
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